

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re Application of: **Levine et al.**

Serial No.: **10/674,606**

Filed: **September 30, 2003**

For: **Method and Apparatus to
Autonomically Take an Exception on
Specified Instructions**

35525

PATENT TRADEMARK OFFICE
CUSTOMER NUMBER

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Group Art Unit: **2183**

Examiner: **Fiegle, Ryan Paul**

Attorney Docket No.: **AUS920030485US1**

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P.O. Box 1450
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Respectfully submitted,

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In re application of:

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Serial No: **10/674,606**

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Intellectual Property
Law Department
International Business
Machines Corporation
11400 Burnet Road
Austin, Texas 78758

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Commissioner for Patents
P.O. Box 1450
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Sir:

Applicants submit herewith patents, publications or other information of which they are aware, which they believe may be material to the patentability of this application and in respect of which there may be a duty to disclose in accordance with 37 C.F.R. 1.56.

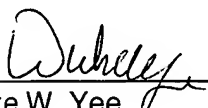
While this Supplemental Information Disclosure Statement may be "material" pursuant to 37 C.F.R. 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to herein is "prior art" for this invention unless specifically designated as such.

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Respectfully submitted,



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Complete if Known

Application Number	10/674,606
Filing Date	09/30/2003
First Named Inventor	Levine et al.
Examiner Name	Fiegle, Ryan Paul
Art Unit	2183
Attorney Docket No.	AUS920030485US1

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

[illegible]

FOREIGN PATENT DOCUMENTS

Examiner Initial	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

NON PATENT LITERATURE

Examiner Initial	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
	AC Short, "Embedded Microprocessor Systems Design: An Introduction Using the Intel 80C188EB" Prentice-Hall, Inc.: 1998, p. 761.

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